KIT-KALAIGNARKARUNANIDHI INSTITUTE OF TECHNOLOGY,CBE-641402

(An Autonomous Institution)

Approved by AICTE Affiliated to Anna University , Chennai

Accredited With 'A' Grade by NAAC & by NBA

EVALUATION OF STUDENT FEEDBACK

Form No: F/TL/15b

Name of the Faculty	Dr.A.Vasantharaj
Designation	:Associate Professor
Department	:ECE
No. of Years of Academic/ Industrial Experience	: 15
Course Code &Name	:B19ECT602 - VLSI Design (VLSI)
Academic year	:2023-24(EVEN)
Branch	:ECE

Date: 29.2.24

Semester / Class : VI/III-B

Feedback Metrics	Regularity & Punctuality of the faculty	Ability of the faculty to Maintain Discipline	Content Delivery is Aligned with the Course Outcomes	Preparation of Course Activity & its Delivery	Encouraging to Raise Doubts and Ability to Clarify	Use of Modern Teaching Pedagogy & Training Aids like Models, Video, Animated CG, PPT's etc.,	Provision of Sufficient Course Materials	Involvement in Internal Assessment
	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded
Excellent (5)	32	33	34	30	34	29	32	34
Very Good (4)	26	24	23	26	20	24	22	21
Good (3)	5	7	7	8	9	10	10	8
Satisfactory (2)	4	3	3	3	4	4	3	3
Poor (1)	1	1	1	1	1	1	1	2
Student response above than the benchmark	58	57	57	56	54	53	54	55
Feedback in %	85.29	83.82	83.82	82.35	79.41	77.94	79.41	80.88
	68	68	68	68	68	68	68	68

Prepared By

Advisor

Reason for below 80%

Due to Circuit designing & Problems in unit 1 & unit-2. Blackboard will be easier to understand.
Subject allotment is scheduled totatto late. So course material provided for only 2 units

Plan of action for implementation

Remaing course materials will be provided in next week.
More pedagogy techniques are planned to implement.

Signature of Faculty:

Advisor

HoD

Corrective Measures/Follow up

Course materials are provided.
Pedagogy techniques used.

Signature of Faculty:

Advisor

HoD

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EVALUATION OF STUDENT FEEDBACK

Form No: F/TL/15b

Name of the Faculty	Dr.A.Vasantharaj
Designation	:Associate Professor
Department	:ECE

Date: 18.4.24

No. of Years of Academic/
Industrial Experience : 15

Course Code &Name :B19ECT602 - VLSI Design (VLSI)

Academic year :2023-24(EVEN)

Branch :ECE

Semester / Class : VI/III-B

Feedback Metrics	Regularity & Punctuality of the faculty	Ability of the faculty to Maintain Discipline	Content Delivery is Aligned with the Course Outcomes	Preparation of Course Activity & its Delivery	Encouraging to Raise Doubts and Ability to Clarify	Use of Modern Teaching Pedagogy & Training Aids like Models, Video, Animated CG, PPT's etc.,	Provision of Sufficient Course Materials	Involvement in Internal Assessment
	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded	No. of Students Responded
Excellent (5)	35	36	33	32	36	32	31	32
Very Good (4)	26	23	23	26	22	24	27	29
Good (3)	4	4	9	7	5	9	7	4
Satisfactory (2)	3	5	3	3	5	3	3	3
Poor (1)	0	0	0	0	0	0	0	0
Student response above than the benchmark	61	59	56	58	58	56	58	61
Feedback in %	89.7	86.76	82.35	85.29	85.29	82.35	85.29	89.71
	68	68	68	68	68	68	68	68

Prepared By

Advisor

Reason for below 80%

Plan of action for implementation

Signature of Faculty:

Advisor

HoD

Corrective Measures/Follow up

Signature of Faculty:

Advisor

HoD